Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1620	(delay adj locked adj loop) and (phase adj detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:11
L2	457	1 and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:11
L3	35	2 and @pd < "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:21
L4	901	DLL and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:21
L5	475	4 and (phase adj detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:22
L6	342	5 and analog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:22
L7	30	5 and @pd <= "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:30
L8	0	(digital adj lock\$3 adj circuit) and (ananlog adj lock\$3 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:31

L9	1822	DLL and (external adj clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:32
L10	266	9 and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:32
L11	162	10 and (phase adj detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:32
L12	6	11 and @pd <= "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:41
L13	1895	skew and (external adj clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:42
L14	577	13 and DLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:42
L15	105	14 and VCO .	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:42
L16	8	15 and @pd <= "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:42

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1620	(delay adj locked adj loop) and (phase adj detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:11
L2	457	1 and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:11
L3	35	2 and @pd < "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:21
L4	901	DLL and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:21
L5	475	4 and (phase adj detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:22
L6	342	5 and analog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:22
L7	30	5 and @pd <= "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:30
L8	0	(digital adj lock\$3 adj circuit) and (ananlog adj lock\$3 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:31

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L9	1822	DLL and (external adj clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:32
L10	266	9 and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:32
L11	162	10 and (phase adj detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:32
L12	6	11 and @pd <= "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:41
L13	1895	skew and (external adj clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:42
L14	577	13 and DLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 10:15
L15	105	14 and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:42
L16	8	15 and @pd <= "19991101"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/07 09:42

L17	48	14 and @pd <= "19991101"	US-PGPUB; USPAT;	OR	OFF	2006/09/07 10:15
			USOCR;			
			EPO; JPO;			
			DERWENT;			
			IBM_TDB			